

10/805, 233

Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>	ATTY. DOCKET NO. 58268-00199	SERIAL NO. Cont. Appln
	APPLICANT Wen-Cheng TSENG et al.	
	FILING DATE March 22, 2004	GROUP 2182

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
ES	AA	5,423,015	06/06/1995	Chung	X	X	X
ES	AB	5,644,784	07/01/1997	Peek			
ES	AC	5,909,686	06/01/1999	Muller et al.			
ES	AD	6,061,351	05/09/2000	Erimli et al.			
ES	AE	5,727,126	03-10-1998	Eichfeld et al.			
ES	AF	5,794,033	08-11-1998	Aldebert et al.			
ES		6,199,192	09-12-2000	Kao et al.			

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5/10/04

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION		
							YES	NO	PART.
ES	AG	99/00936	01/07/1999	PCT	120			X	
	AH								
	AI								
	AJ								
	AK								
	AL								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

AM	Ian Page, "Reconfigurable Processor Architectures," Microprocessors and Microsystems, IPC Business Press Ltd., London, Great Britain, Vol. 20, No. 3, 1 May 1996, p. 185-196.
AN	
AO	

EXAMINER

/Eron Sorrell/

DATE CONSIDERED

01/05/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.